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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Jarrett L. RINALDI, et al.

Serial No.:

09/965,234

Group Art Unit:

2827

Filed:

September 25, 2001

Examiner:

J. Mitchell

FOR:

MULTI-STACK SURFACE MOUNT LIGHT EMITTING DIODES

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents Washington, DC 20231

Sir:

In response to the Office Action mailed August 6, 2002, applicants hereby elect the claims of Group I, namely claims 1-12, for prosecution in the present application. By way of a preliminary amendment filed concurrently herewith, applicants have canceled the non-elected claims 13-20 and added new claims 21-28. New claims 21-28 are readable on the elected invention.

If there are any questions regarding the present application, the Examiner is invited to contact the undersigned attorney at the telephone number listed below. Examination on the merits is awaited.

Respectfully submitted,

September 6, 2002

Date

Intel Americas LF1-102 4050 Lafayette Center Drive Chantilly, VA 20151 Paul E. Steiner

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I hereby certify that this correct the United States Postal Se postage in an envelope add eing deposited with mail with sufficient commissioner of Patents,

Washington, D.C. 20231 on:

9.4.02 Date of

Name of Person Maii..., ... responde

Signature

Date